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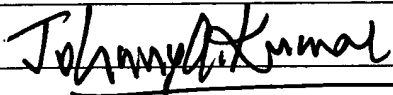


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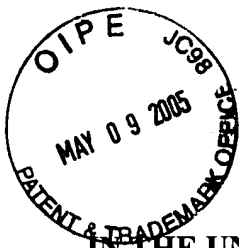
TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application Number	10/608,426
	Filing Date	June 30, 2003
	First Named Inventor	Jung Taik CHEONG et al.
	Group Art Unit	2813
	Examiner Name	Stephen W. Smoot
Total Number of Pages in This Submission	Attorney Docket Number	40296-0029

ENCLOSURES (check all that apply)		
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Attorney Docket No.: 40296-0029

Applicant(s): Jung Taik Cheong et al

Confirmation No.: 8083

Appl. No.: 10/608,426

Examiner: Stephen W. Smoot

Filing Date: June 30, 2003

Art Unit: 2813

Title: METHOD FOR FORMING A SEMICONDUCTOR DEVICE WITH A
HARD MASK LAYER FORMED OVER A BIT LINE TO PROTECT
THE BIT LINE DURING SUBSEQUENT ETCHING STEPS

TRANSMITTAL OF CORRECTED DRAWINGS

Commissioner for Patents
P.O. Box 1450
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Sir:

Applicants submit herewith Figures 1b-1c, 2c-2d, 3a-3e and 4d-4e (6 sheets) in response to the Examiner's objection in the Notice of Allowability mailed February 8, 2005. Applicants believe these drawings are acceptable under M.P.E.P. §608.02(VII). Applicants respectfully request approve of these drawings for entry into the application.

Respectfully submitted,

Date: 5/9/05

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